

FIG.1

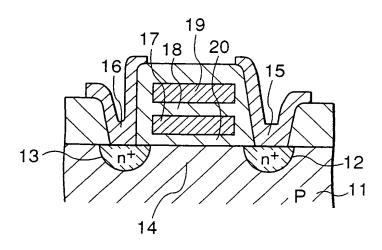


FIG.2

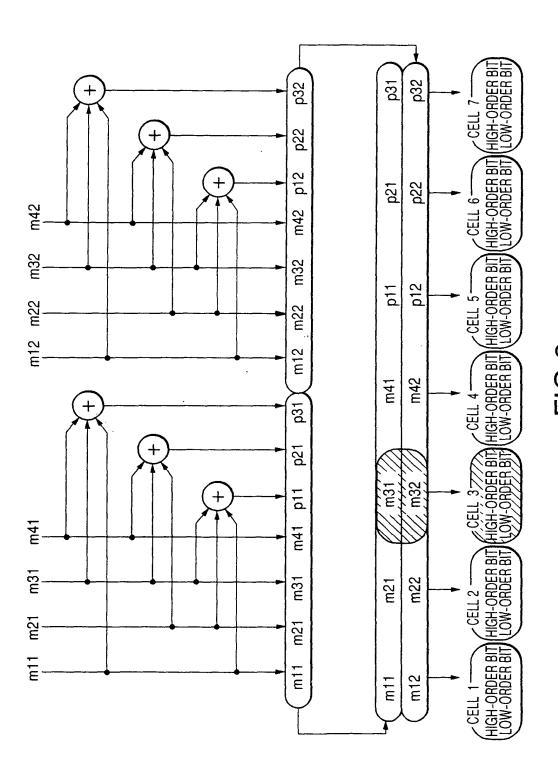


FIG.3

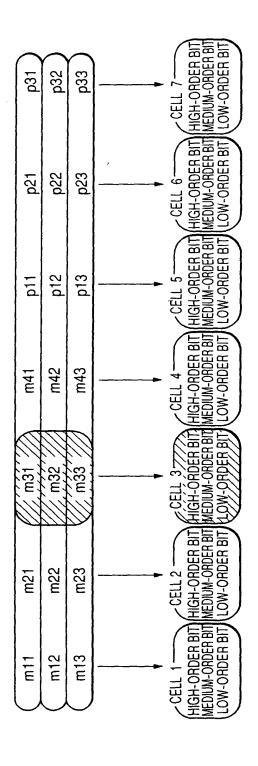
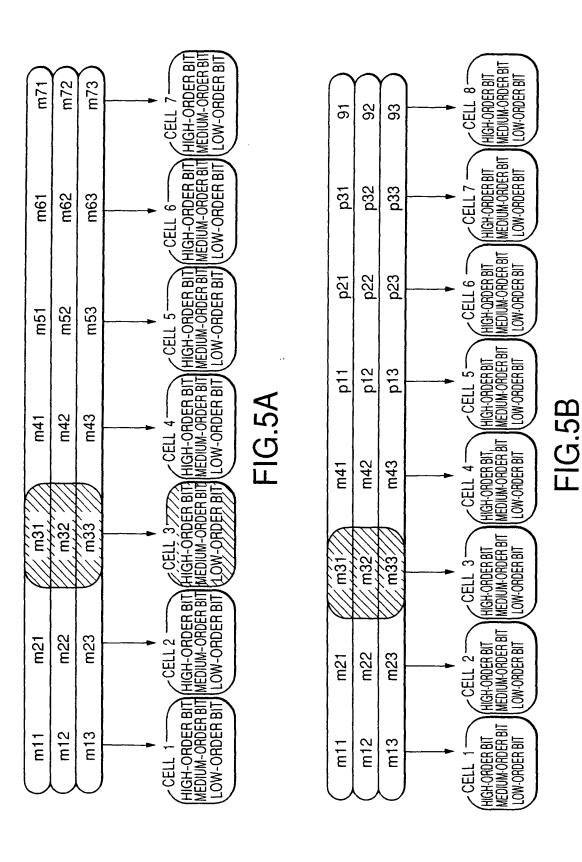


FIG.4



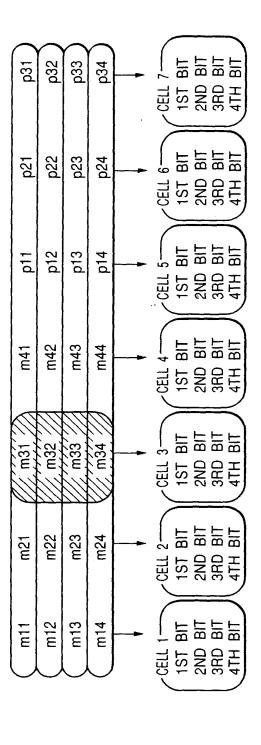


FIG.6

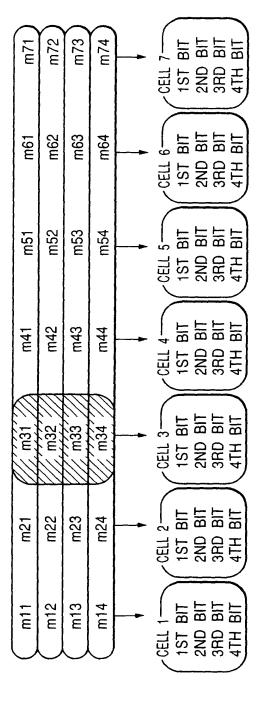


FIG.7A

91	95	88	99		8 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
6	0	් ර	Ó		CELL 8-15T BIT 2ND BIT 3RD BIT 4TH BIT
p31	p32	p33	p34p	-	CELL 7 T 1ST BIT 2ND BIT 3RD BIT 4TH BIT
p21	p22	p23	p24		CELL 6 1ST BIT 2ND BIT 3RD BIT 4TH BIT
p11	p12	p13	p14		CELLS 1ST BIT 2ND BIT 3RD BIT 4TH BIT
) m41	m42	m43) m44	-	CELL 4 1ST BIT 2ND BIT 3RD BIT 4TH BIT
/ m31 /	m32 //	m33 //	K m34 //		CELL 3 1ST BIT 2ND BIT 3RD BIT 4TH BIT
m21	m22	m23	m24	-	CELL 2 1ST BIT 2ND BIT 3RD BIT 4TH BIT
m11	(m12	(m13	m14		CELL 1 1ST BIT 2ND BIT 3RD BIT 4TH BIT

FIG.7B

